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Application Number	10005551		
Filing Date	Nov 8, 2001		
First Named Inventor	Santanu Dutta		
Title	HIGH-SPEED COMPUTATION IN ARITHMETIC LOGIC CIRCUIT		
Art Unit	2193		
Examiner Name	MALZAHN, DAVID		
Attorney Docket Number	US018181		

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OR Assignee of record of the entire interest. See 37 CFR 3.71.					
Statement under 37 CFR 3.73(b) (Form PTO/SB/96) submitted herewith or filed on					
	SIGNATURE of Applicant o	Assignee o	f Record		
Signature	/Peter Zawilski/		Date	Nov 11, 2008	
Name	Peter S. Zawilski, Reg. No. 43,305 Telephone (408) 474-9063		(408) 474-9063		
Title and Company	Authorized Representative, NXP, B.V.				
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.					
*Total of1 forms are submitted.					

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STATEMENT UNDER 37 CFR 3.73(b)				
Applicant/Patent Owner: NXP, B.V.				
Application No./Patent No.: 10005551	_ Filed/Issue Date: Nov 8, 2001			
Entitled: HIGH-SPEED COMPUTATION IN AR	ITHMETIC LOGIC CIRCUIT			
NXP, B.V.	, a corporation			
(Name of Assignee)	(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)			
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2. an assignee of less than the entire right, title at (The extent (by percentage) of its ownership in	nd interest nterest is %)			
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	original assignment document(s)) must be submitted to Assignment to record the assignment in the records of the USPTO. See MPEP			
The undersigned (whose title is supplied below) is au	uthorized to act on behalf of the assignee.			
/Peter Zawilski/	Nov 11, 2008			
Signature	Date			
Peter S. Zawilski, Reg. No				
Printed or Typed Name	e Telephone Number			
Authorized Representa	ative			
Title				

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